

In the Claims:

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1. (Original) An in-plane switching mode liquid crystal display device comprising:
 - a plurality of data lines for applying data signals to a thin film transistor array;
 - a plurality of gate lines for applying gate signals to the thin film transistor array;
 - a plurality of gate links extended from the plurality of gate lines into an outer area of the thin film transistor array; and
 - a plurality of common voltage lines, being provided in such a manner to cross the plurality of gate links, for applying a common voltage to the thin film transistor array.
 2. (Original) The in-plane switching liquid crystal display device according to claim 1, wherein the common voltage lines are provided at the thin film transistor array to be in parallel to the gate lines.
 3. (Original) The in-plane switching liquid crystal display device according to claim 1, wherein the common voltage is approximately +5V.
 4. (Original) The in-plane switching liquid crystal display device according to claim 1, wherein the gate signals include a gate low voltage signal of approximately -5V.
 5. (Original) The in-plane switching liquid crystal display device according to claim 1, wherein the gate signals include a gate high voltage signal of approximately +20V.
 6. (Original) An in-plane switching mode liquid crystal display device, comprising:
 - first and second substrates;
 - a plurality of data lines;
 - a plurality of gate lines perpendicular to the data lines;

a plurality of thin film transistors at crossing points of the data and gate lines and forming a thin film transistor array on the first substrate;

a plurality of gate links extended from the plurality of gate lines into an area outside of the thin film transistor array; and

a plurality of common voltage lines parallel to the gate lines and crossing the gate links.

7. (Original) The in-plane switching liquid crystal display device of claim 6, further comprising:

a plurality of gate pads connected to the gate links and electrically disposed between the gate links and an external power source; and

a plurality of common voltage pads connected to the common voltage lines and electrically disposed between the common voltage lines and the external power source.

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8. (Original) The in-plane switching liquid crystal display device of claim 7, wherein the gate pads are located in the area outside of the thin film transistor array.

9. (Original) The in-plane switching liquid crystal display device of claim 7, wherein the common voltage pads are located in the area outside of the thin film transistor array.

10. (Amended) An in-plane switching mode liquid crystal display device comprising:
a plurality of data lines for applying data signals to a thin film transistor array;
a plurality of gate lines for applying gate signals to the thin film transistor array;
a plurality of signal pads in a signal pad area outside the thin film transistor array; and
a plurality of common lines[, at least one of said common lines] extending between the signal pads and the thin film transistor array.

11. (Original) The in-plane switching liquid crystal display device of claim 10, wherein the signal pads are gate pads for applying the gate signals to the gate lines.

12. (Original) The in-plane switching liquid crystal display device of claim 11, wherein the at least one common line crosses the gate lines in an area between the gate pads and the thin film transistor array.

13. (Original) The in-plane switching liquid crystal display device of claim 11, wherein the at least one common line is parallel to the data lines.

14. (Original) The in-plane switching liquid crystal display device of claim 10, wherein the signal pads are data pads for applying the data signals to the data lines.

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15. (Original) The in-plane switching liquid crystal display device of claim 14, wherein the at least one common line crosses the data lines in an area between the data pads and the thin film transistor array.

16. (Original) The in-plane switching liquid crystal display device of claim 14, wherein the at least one common line is parallel to the data lines.
